

1. Contact

Residence:

Jinshan village, Huzhou city, Zhejiang province, China

Mobile:

0086 159 6825 2315

Email:

neoworldmark@tutanota.com

Web:

<https://sites.google.com/site/markchen66666/>

2. EDUCATION & ACADEMIA

2.1. Master of Engineering in Thermophysics

Tongji University, 1986-1989, Shanghai, China

2.2. Others

PhD candidacy for several universities (1991-1992), incl:

- ◆ University of Glasgow (PhD for gas turbine combustion research)
- ◆ Université de Pierre et Marie Curie in Paris (University of Paris VII), (DEA de Cinématique Chimique Appliquée, option catalyse, Laboratoire de Réactivité de Surface et Structure),

3. BUSINESS

3.1. VLSI Design

2017 – today Angelia Technologies, Founder, VLSI Design

Design of (satellite broadband) modem IC.

Development and operation of an industry portal for the satellite broadband Internet modem IC industry.

See <http://www.angelia.space/> for details.

3.2. Other Businesses

2000 – 2017 NeoTech Consultants / Solaires Power International / HeliosLab, Founder or Cofounder, Various technical consulting and information services in several sectors incl:

Power consulting / Internet security, encryption and cryptographic services / Language services.

4. CAREER

1997.11 – 1999.11 Deutsche Babcock China (Beijing, China), Global Procurement Manager

1996.5 – 1997.10 ALSTOM East China (Shanghai, China) , Electrical Engineer focusing on Marketing

1994.11 – 1996.4 TÜV Rheinland (Shanghai, China), (ISO 9000 Quality Certification) Project Engineer

1992.6 – 1994.6 Siemens Heimann Optoelectronics (Shenzhen, China), Quality Dept Manager

5. RESEARCHES & STUDIES

5.1. IC research for upcoming satellite broadband Internet modem ICs (2017 - today)

Trying to figure out a universal modem IC for all bandwidth and all modulation and demodulation technologies.

5.2. VLSI/Chip related researches (2013 - 2017)

New generation of IC technologies (2015-2017):

Reviewed and explored dozens of potential post-Moore IC technologies including but not limited to: quantum chip, in particular superconducting quantum circuits / computer, spintronics based IC, DNA computing, Neural computing, 3d IC etc.

Graph theory(particularly graph embedding) for conventional silicon based IC design (2013-2015).

5.3. Other researches (2007 to 2013)

- Mathematics (2010 to 2017) - Number theory related to cryptology, numerical computation
- Fluidynamics (2010)
- Thermal Physics & Thermodynamics (2008-2009)
- Solar related (solar physics, solar thermodynamics, solar PV etc) (2007-2008)

6. Other Useful Skills

LANGUAGES

English near native level, German good, and French reading comprehension.

IT & INTERNET

Familiar with key IT technologies, incl programming, databases, web scraping etc.

